

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT APPLICATION

Applicant Seong-hoon Lee

Application No. : 10/734,506 Confirmation No.: 1009

Filed December 12, 2003

For GENERATING MULTI-PHASE CLOCK SIGNALS

USING HIERARCHICAL DELAYS

Group Art Unit : 2816

Mail Stop MISSING PARTS Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

SUBMISSION OF FORMAL DRAWINGS

Sir:

Pursuant to 37 C.F.R. §§ 1.84 and 1.121, applicant herewith furnishes six (6) sheets of formal drawings to be substituted for the informal drawings filed with this application.

Respectfully submitted,

Peter F ≴nell.

Registration No. 52,235

Agent for Applicant

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